

METHOD AND SYSTEM FOR CREATING SECURE ADDRESS SPACE
USING HARDWARE MEMORY ROUTER

ABSTRACT OF THE DISCLOSURE

5 A method and system providing dynamic allocation of memory through
hardware is disclosed. An embodiment provides for a multi-processor system
providing for a secure partitioned memory. The system comprises a
processor(s), a hardware implemented memory router coupled to the
processor(s), and memory coupled to the memory router. The memory router
stores memory partition information, which describes the memory allocated to
10 the processor(s). Furthermore, the memory router maps a memory access
request from a processor to an address in the memory.

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